

Due to the recent regulation of TalTech rectorate, the labs for 'IAX0600 Digital Systems Design' cannot take place at their regular times in ICT-501 until February 7 (or until further notice). Still, the labs will continue as scheduled in the form of distance learning. During this time the working process is changed as follows:

- Lecture 1 will not take place on January 26 as originally scheduled. It will be pushed to February 2 instead. The lecture will be given over MS Teams.
- Tutorial (Part 1) will take place on February 3 as originally scheduled. The lab will be given over MS Teams.
- In preparation to Tutorial (Part 1) students should download and install the free WebPack version of Xilinx Vivado tool (instructions are available on the webpage of the lab course). It is recommended to use Vivado 2020.1, since this version is installed on the lab PCs. This may also be needed should the distance learning requirement continue (or is reinstated at a later time).
- Alternatively, it may be possible to remotely connect to the ICT-501 classroom PCs (e.g. as it is described in 'General Instructions' for the 'IAS0340 Digital Systems Modeling and Synthesis' course at <http://ati.ttu.ee/IAS0340/labs/GI.html>).
- Using WebPack version of Xilinx Vivado (or using remote connection) is should be possible to go through the whole design flow for each lab (create source files, perform functional simulation, generate bitstream file).
- Any lab related questions/problems can be communicated to the teaching staff via e-mail (dmitri.mihhailov@taltech.ee, aleksander.sudnitson@taltech.ee).