AMEER SHALABI

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Tallinn University of Technology

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EDUCATION

Tallinn University of Technology   *Sept. 2019 – Current*
Doctor of Philosophy - PhD, Information and Communications Technology   *Tallinn, Estonia*
· Early Stage Researcher of the Center for Dependable Computing Systems
· Visiting Lecturer of Computer Systems

Tallinn University of Technology   *Sept. 2017 – June 2019*
Master of Science - MSc, Computer and Systems Engineering   *Tallinn, Estonia*
· Member of the Center for Dependable Computing Systems

Bard College   *Aug. 2012 - May 2016*
Bachelor of Arts - BA, Computer Science   *Annandle-on-Hudson, NY*
· Member of the Center for Dependable Computing Systems

EXPERIENCE

Center for Dependable Computing Systems at TalTech   *Sept. 2019 – Current*
Early Stage Researcher   *Tallinn, Estonia*
· Researching security and reliability of high efficiency Computer Memory Systems.
· Researching in the field of embedded systems design and implementation.
· Assisting and cooperating with members of Center for Dependable Computing Systems on research of Reliability and Dependability of computer Systems.

Department of Computer Systems - TalTech   *Sept. 2019 – Current*
Visiting Lecturer   *Tallinn, Estonia*
· Designing and Lecturing the IAS0430 Microprocessor Systems course during the Winter semester.
· Working closely with department head and staff on assisting with the development of both Introductory and Advanced Computer Architecture courses.

Center for Dependable Computing Systems at TalTech   *Sept. 2018 – May 2019*
Research Intern   *Tallinn, Estonia*
· Researched topics of computer systems verification and computer architecture design and validation.
· Explored topics of Non-Volatile Memory (NVM) and Computation in Memory (CiM) in preparation for the master thesis.

SKILLS AND STRENGTHS

Natural Languages	Arabic (Native Language), English (Fluent)
Programming Languages	**Experienced:** · VHDL, Verilog, SysVerilog, Bash Script, Tex, MATLAB **Intermediate:** · SQL, Python, Java, C, C++, TCL **Basic:** · R, HTML, CSS, Prolog, Processing
Tools	**Experienced:** · Synopsys, Cadence tools, ModelSim, Overleaf, MS office, Moodle · Linux Command Line **Intermediate:** · Vivado, github, Visual Studio, SPICE, Google Docs, VirtualBox **Basic:** · Looker, PyCharm, Tableau, Power BI

CERTIFICATIONS AND TRAINING

imec academy
- VHDL language and design flow – Oct. 2019
- Essential verification with SystemVerilog – Nov. 2019 (issued 2021)

MathWorks
- MATLAB Fundamentals – Jan. 2021

LinkedIn Learning
- Become a SQL Developer – Jan. 2021
- Become a Data Analyst – Jan. 2021
- Become a Data Scientist – Feb. 2021

Coursera
- Introduction to Data Science in Python – Feb. 2021
- Very Large Scale Integrated Circuit Computer Aided Design (VLSI CAD) – Ongoing.

For credentials and further information, please see LinkedIn profile.

PUBLICATIONS

[1] A. Shalabi, *FLD: A FIFO/LRU Decision Algorithm for Managing L1 Private Cache in Heterogeneous Processors.* Bachelor's thesis, Bard College, 2016.

[2] A. Shalabi, *Performance and Energy Evaluation of NVM-Based CIM and Memory Hierarchy.* Master's thesis, Tallinn University of Technology, 2019.

[3] A. Shalabi, K. Paul, T. Ghasempouri, and J. Raik, "NV-SP: A New High Performance and Low Energy NVM-Based Scratch Pad," in *2020 IEEE Computer Society Annual Symposium on VLSI (ISVLSI)*, (Los Alamitos, CA, USA), pp. 54–59, IEEE Computer Society, 2020.

[4] A. Shalabi, T. Ghasempouri, P. Ellervee, and J. Raik, "SCAAT: Secure Cache Alternative Address Table for mitigating cache logical side-channel attacks," in *2020 23rd Euromicro Conference on Digital System Design (DSD)*, (Kranj, Slovenia), pp. 213–217, IEEE, 2020.